# AK8789

Shipped in packet-tape reel(5000pcs/Reel)

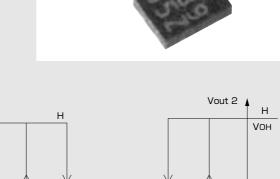
AK8789 is ultra-small Hall effect IC of a single silicon chip composed of Hall element and a signal processing IC.

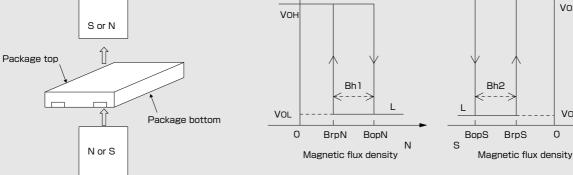
Unipolar Hall Effect Switch Two output for S and N-pole	Supply Voltage 1.6~5.5V	Hall Element Pulse Excitation	High Sensitivity Bop:2.5mT	Output CMOS Two output for S and N-pole	SON
Notice: It is requested to r	ead and accept "IMPOR	TANT NOTICE" written or	n the back of the front cov	er of this catalogue.	

# Features

- · High sensitive omnipolar operation
- · Dual output
- Micropower operation
- Typ.6.5µA (average:VDD=1.85V)
- Ultra small SON package : 1.1×1.4×t0.37mm Halogen free

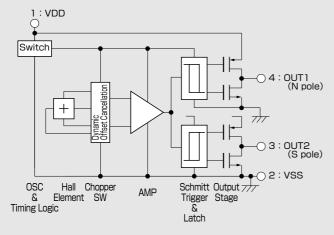






Vout1

# Functional Block Diagram



Item	Function			
OSC	Generates operating clock			
Timing logic	Generates timing signal requires for Chopper SW, AMP and COMP			
Hall Element	Hall element fabricated by CMOS process			
Chopper SW	Performs chopping in order to cancel the offset voltage of Hall sensor			
AMP	Reduce offset voltage and amplifies Hall output voltage			
Schmitt Trigger	Hysteresis comparator			
Output Stage	CMOS output, During the power down mode, output is latched in its previous state			

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# ASAHI KASEI MICRODEVICES

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#### Absolute Maximum Ratings

Item	symbol	Min.	Max.	Unit
Power supply voltage	V <sub>DD</sub>	-0.3	+6.5	V
Output current	I <sub>OUT</sub>	-0.5	+0.5	mA
Storage temperature	T <sub>STG</sub>	-55	+125	Ĵ

Note: Stresses beyond these listed values may cause permanent damage to the device.

#### Recommended Operating Conditions

Item	symbol	Min.	Typ.	Max.	Unit
Power supply voltage	V <sub>DD</sub>	1.6	1.85	5.5	
Operating temperature	T <sub>a</sub>	-30		+85	

#### ●Electrical Characteristics (Ta=25℃ VDD=1.85V)

Item	symbol	Min.	Тур.	Max.	Unit	Note
Current consumption	I <sub>DD1</sub>		6.5	9	μA	Average
High level output voltage	V <sub>OH</sub>	$V_{DD}$ -0.4			V	I <sub>out</sub> =-0.5mA
Low level output voltage	V <sub>OL</sub>			0.4	V	I <sub>out</sub> =+0.5mA
Pulse drive period	T <sub>PD1</sub>	25	50	100	ms	
Pulse drive time	T <sub>PD2</sub>	73	146	220	μs	

## ●Magnetic Characteristics① (Ta=25°C VDD=1.85V)

Item	symbol	Min.	Тур.	Max.	Unit
On anothing and in the	B <sub>op</sub> N	*1.4	2.5	3.2	mT
Operating points	B <sub>op</sub> S	-3.2	-2.5	*-1.4	mT
Releasing points	B <sub>rp</sub> N	1.2	2.0	*3.0	mT
	B <sub>rp</sub> S	*-3.0	-2.0	-1.2	mT
Hysteresis	BhN,BhS	*0.1	0.5		mT

The characteristics with \* marks are design targets.

#### ●Magnetic Characteristics②(Ta=-30℃~85℃ VDD=1.6~5.5V)

ltem	symbol	Min.	Тур.	Max.	Unit
Or creating a cinter	B <sub>op</sub> N	1.3	2.5	3.5	mT
Operating points	B <sub>op</sub> S	-3.5	-2.5	-1.3	mT
D. L	B <sub>rp</sub> N	1.1	2.0	3.3	mT
Releasing points	B <sub>rp</sub> S	-3.3	-2.0	-1.1	mT
Hysteresis	BhN,BhS	0.1	0.5		mT

The specifications in Magnetic Characteristics 2 are design targets.

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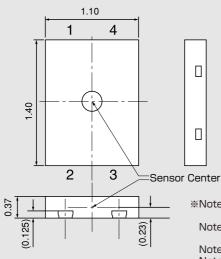
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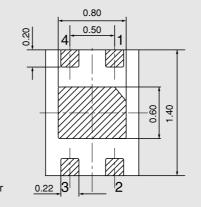
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#### Package (Unit:mm)





%Note 1) Sensitive area position referenced to the center of package within  $\phi$ 0.3mm circle. Note 2) Tolerances of dimension otherwise noted is

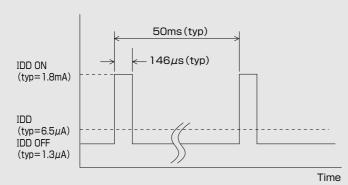
±0.05mm.

Note 3) Hatched area is plated.

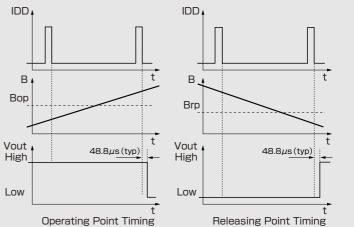
Note 4) Center pad area (TAB) should be tied to the VSS or floating

No.	Pin name	Function	Note
1	VDD	Power supply pin	
2	VSS	Ground pin	
3	OUT2	S pole detection output pin	CMOS Output*
4	OUTI	N pole detection output pin	CMOS Output*

# ●IDD Timing Chart



Functional Timing Chart



Note: Hall IC's output is held as internal data just before the internal circuit turns off. And after 48.8ms (typ.) the output changes.

#### Footprint (for reference)

 $0.35^{\pm 0.10}$ 

0.22<sup>±0.05</sup>

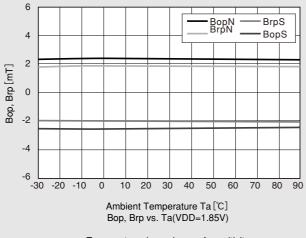
0.80

1.70

0.60

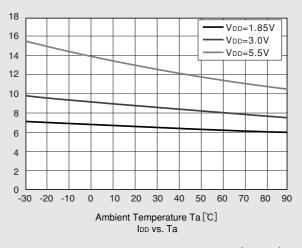
 $0.50^{\pm0.05}$ 

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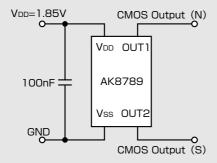
# Typical Characteristics Data (for reference)





Temperature dependence of current consumption (Average)

## Application Circuit



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